





ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	NON-VOLATILE TRANSISTOR MEMORY ARRAY INCORPORATING READ-ONLY ELEMENTS WITH SINGLE MASK SET																				
<p>Application Number: 10/810035 </p> <p>Confirmation Number: 8458</p> <p>First Named Applicant: Albert Weiner</p> <p>Attorney Docket Number: ATM-291</p> <p>Art Unit: 2818</p> <p>Search string: (6396767).pn.</p> <p>US Patent Documents</p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1" style="width: 100%;"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td>DM</td><td>1</td><td>6396767</td><td>2002-05-28</td><td>Denny Tzeng et al.</td><td>B1</td><td>365</td><td>230.06</td></tr></tbody></table> <p>Signature </p> <table border="1" style="width: 100%;"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td>THINH T. NGUYEN</td><td>8/18/2005</td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass	DM	1	6396767	2002-05-28	Denny Tzeng et al.	B1	365	230.06	Examiner Name	Date	THINH T. NGUYEN	8/18/2005
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass														
DM	1	6396767	2002-05-28	Denny Tzeng et al.	B1	365	230.06														
Examiner Name	Date																				
THINH T. NGUYEN	8/18/2005																				